

FIGURE 1  
PRIOR ART CACHE

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BEST AVAILABLE COPY

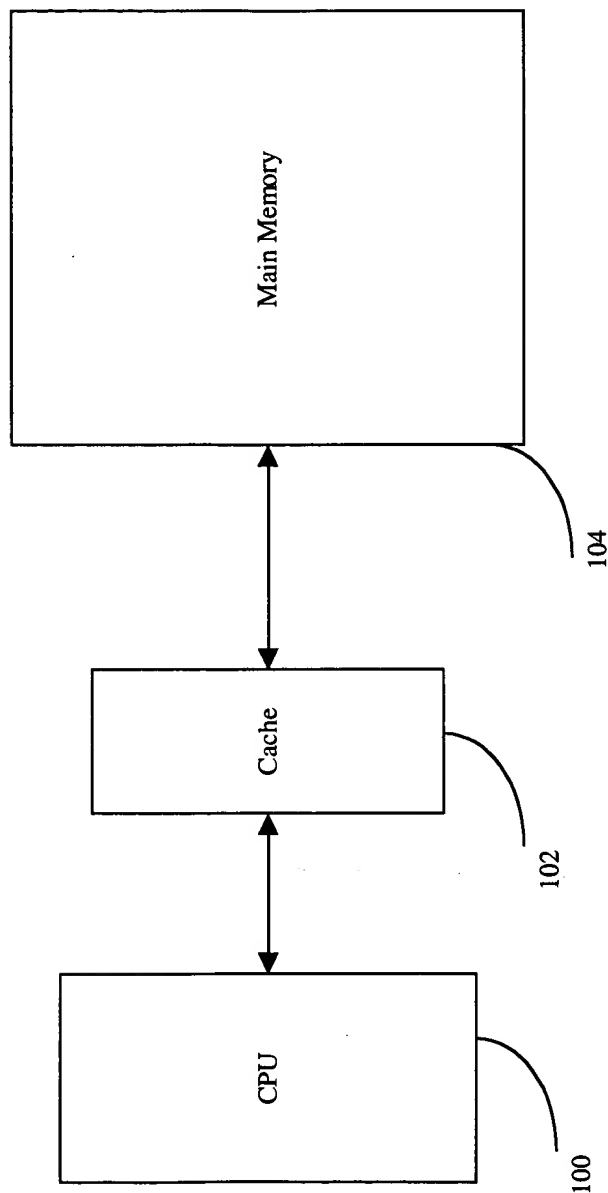
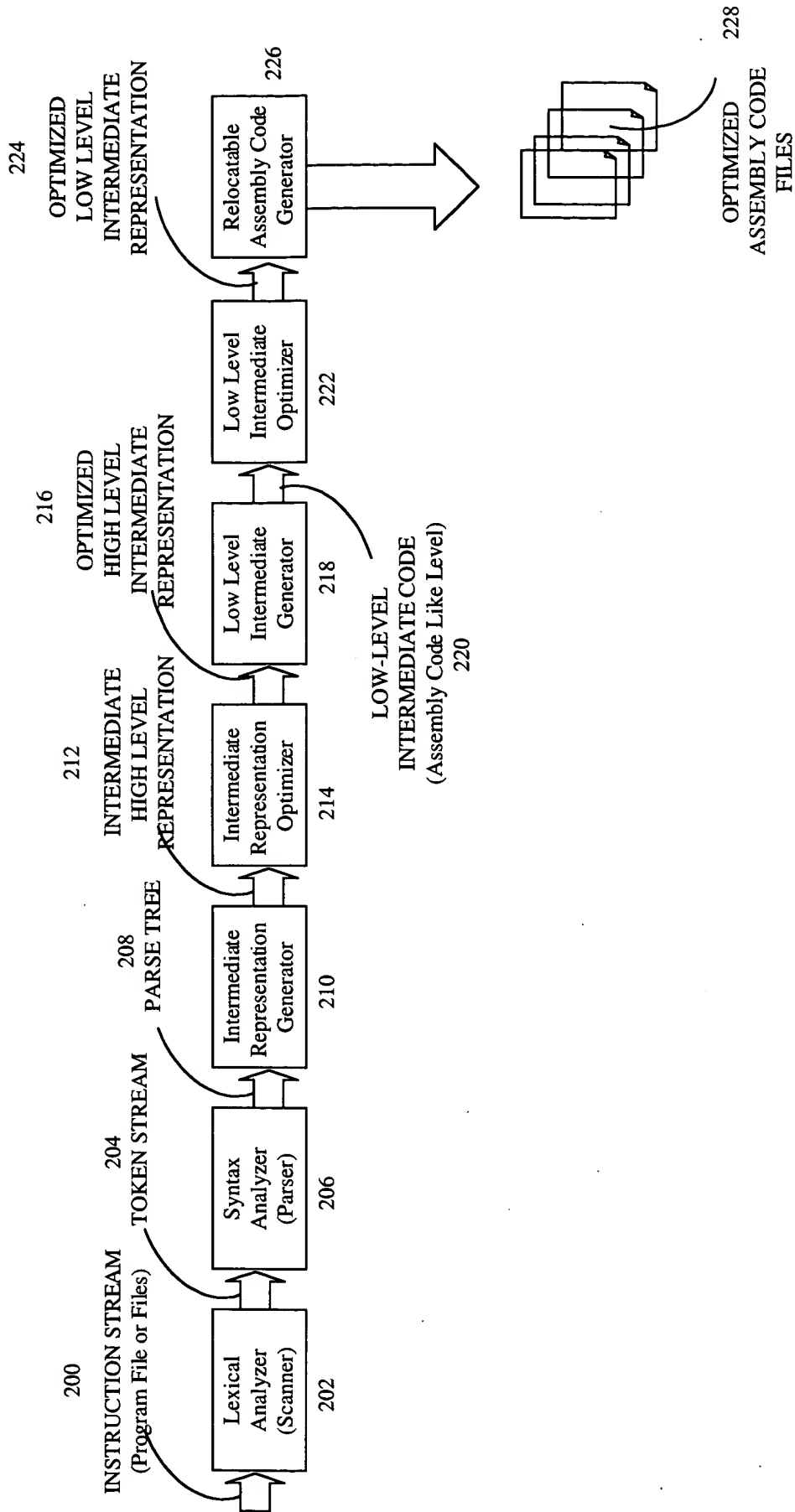


FIGURE 2  
TYPICAL COMPILER



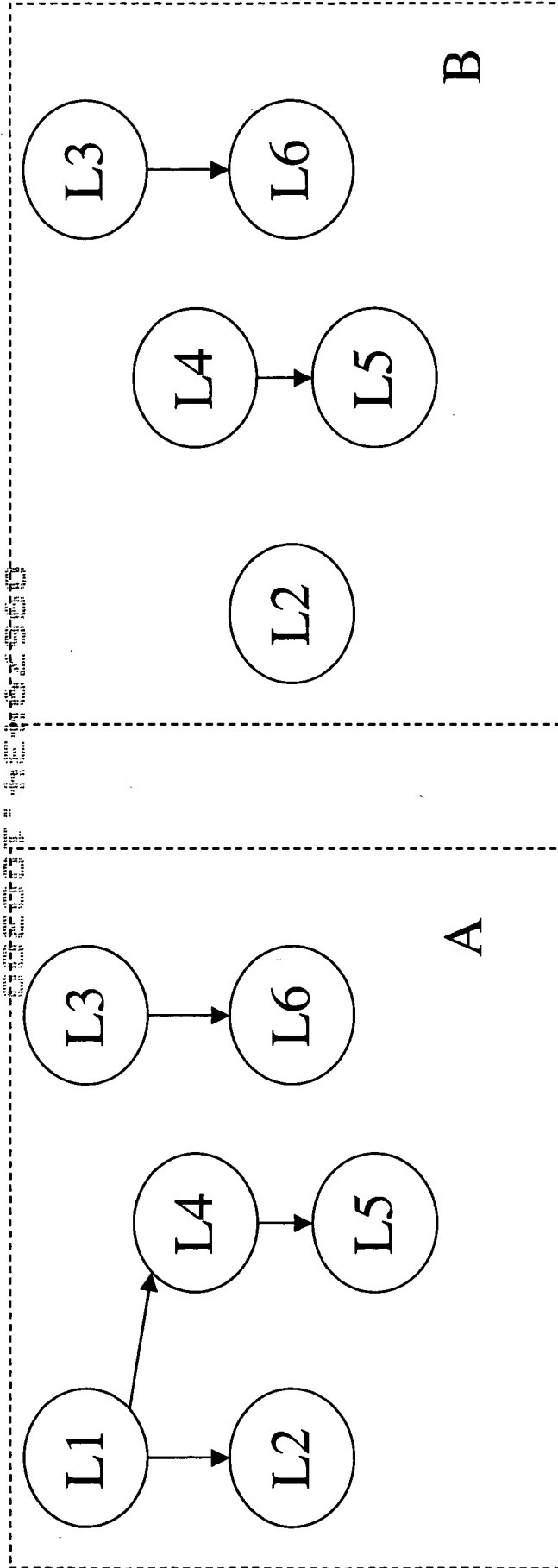


FIGURE 3: Directed Acyclic Graphs  
According To The Present Invention

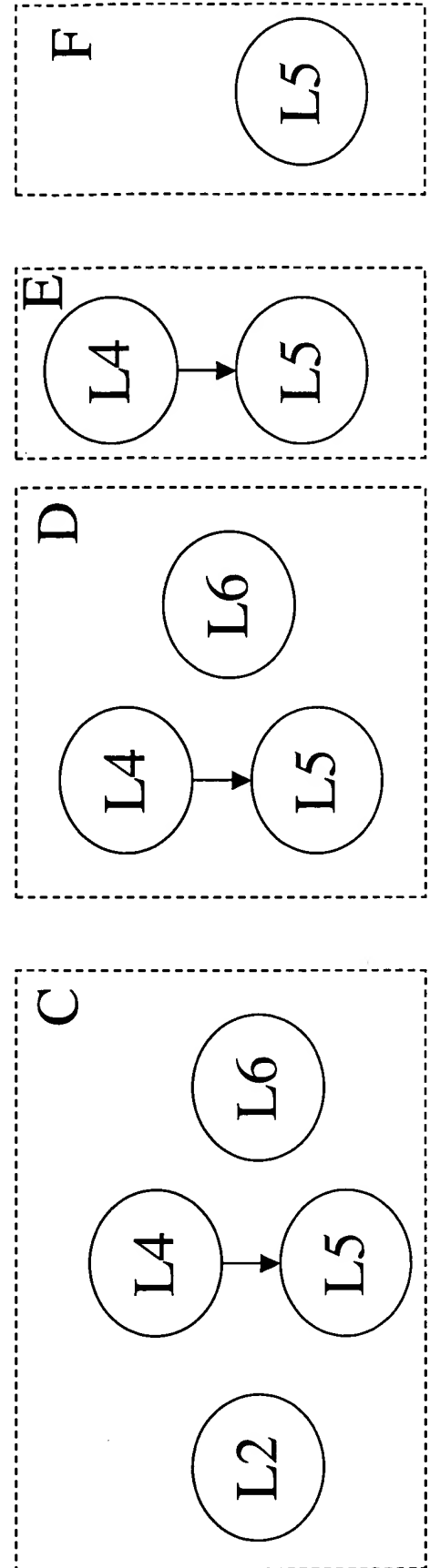


FIGURE 4  
SYSTEM WITH  
COMPILER ACCORDING  
TO THE CURRENT INVNETION

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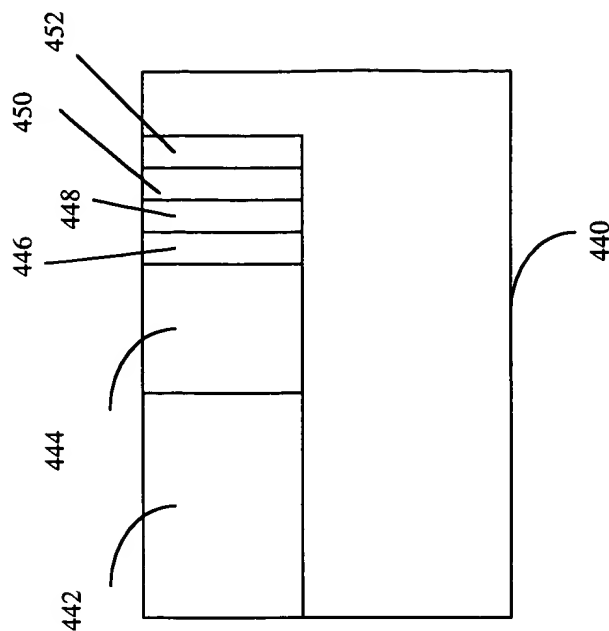
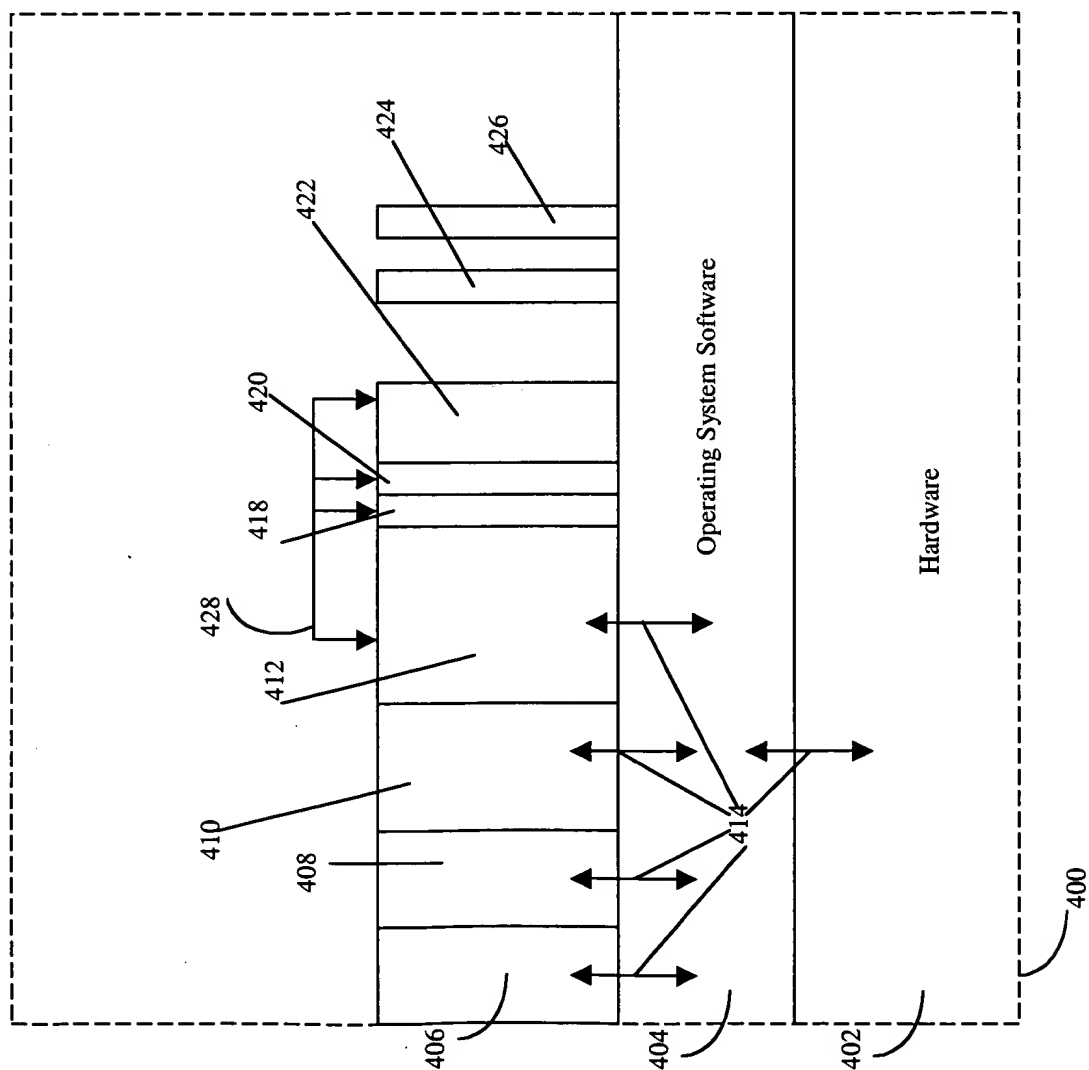


FIGURE 5  
COMPILER  
ACCORDING TO  
THE PRESENT  
INVENTION

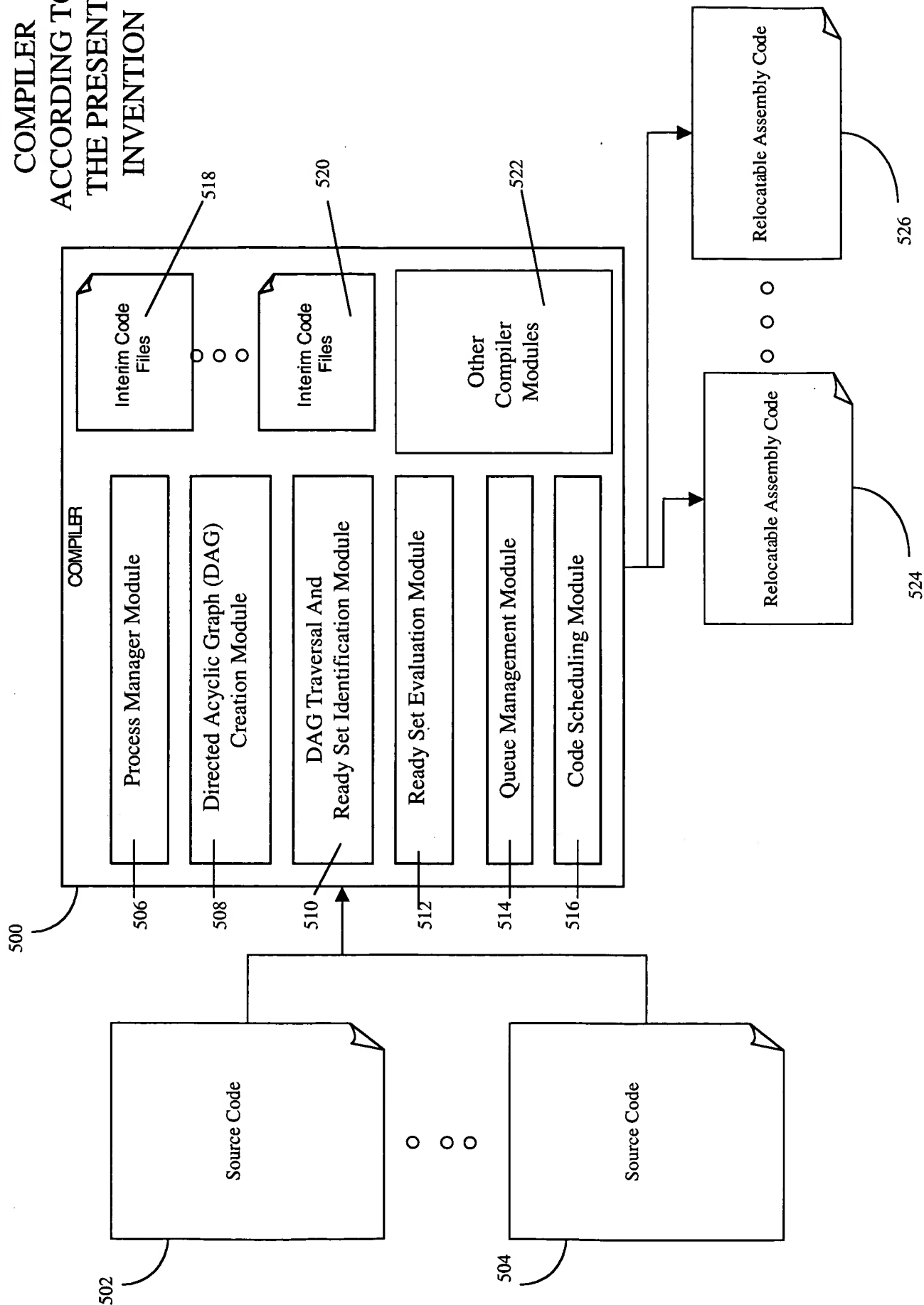


FIGURE 6  
MEMORY INSTRUCTION  
SCHEDULING  
METHOD

